

<IGBT Modules>

## CM600DX-24T/CM600DXP-24T

HIGH POWER SWITCHING USE **INSULATED TYPE** 



Collector current Ic ..... 600A Maximum junction temperature T<sub>vjmax</sub> ....... 175°C

- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- •Tin-plating pin terminals



Collector current Ic ..... Maximum junction temperature T<sub>vjmax</sub> ....... 175°C

- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

#### **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

#### **OPTION** (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- V<sub>CEsat</sub> selection for parallel connection

#### **INTERNAL CONNECTION**

#### **TERMINAL CODE** 1. TH1 6. C2E1 2. TH2 7. C2E1 3. G1 8. G2 4. Es1 9. Es2 Di2 5. Cs1 10. E2 11. C1 NTC

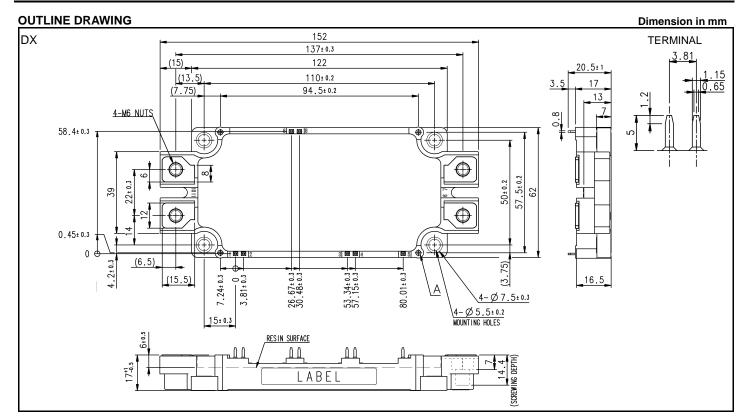
### **OUTLINE DRAWING** Dimension in mm MOUNTING HOLES SECTION A COM. <u>(6.5)</u> Ø2.6 Ø2.32 28 働

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HIGH POWER SWITCHING USE

INSULATED TYPE

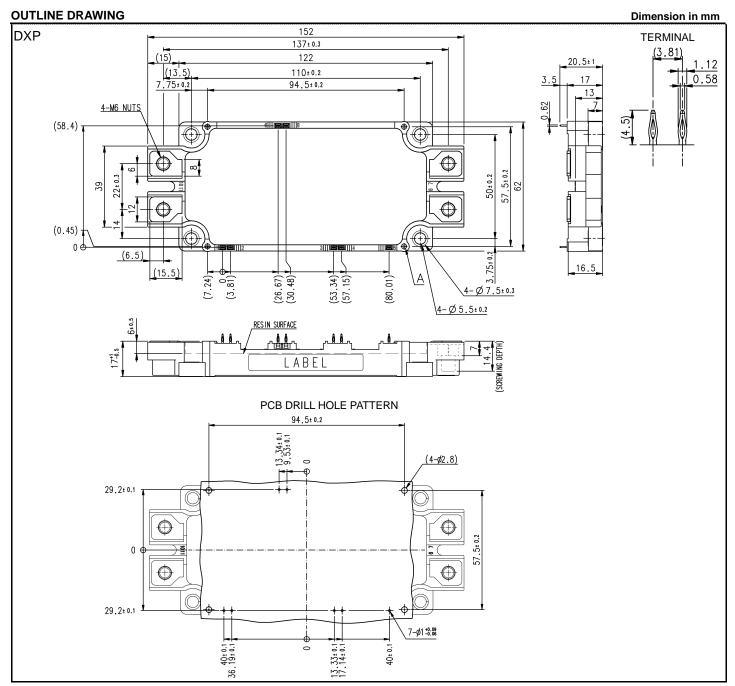


Tolerance otherwise specified

Divisio	n of	Tolerance		
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE



Tolerance otherwise specified

Divisio	n of	Tolerance					
	0.5	to	3	±0.2			
over 3		to	6	±0.3			
over	6	to	30	±0.5			
over	30	to 120		±0.8			
over 120		to 400		±1.2			

HIGH POWER SWITCHING USE

INSULATED TYPE

#### MAXIMUM RATINGS ( $T_{vj}$ =25 °C, unless otherwise specified)

#### INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V	
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T <sub>C</sub> =114 °C (Note2, 4)	600	Λ	
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	1200	A	
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	3125	W	
I <sub>E</sub> (Note1)	Emitter current	DC (Note2)	600	^	
I <sub>ERM</sub> (Note1)	Emilier current	Pulse, Repetitive (Note3)	1200	Α	

#### **MODULE**

Symbol	Item Conditions		Rating	Unit
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T <sub>vjmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	
T <sub>vjop</sub>	Operating junction temperature	Continuous operation (under switching)	-40 ~ <b>+</b> 150	°C
T <sub>stq</sub>	Storage temperature	-	-40 ~ +125	

## ELECTRICAL CHARACTERISTICS ( $T_{vj}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol Item		Conditions		Limits			Linit
Symbol	item	Conditions			Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited			1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	Ic=60 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.65	2.05	
V <sub>CEsat</sub> (Terminal)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.85	-	V
(Terminal)	Callantar are the restriction value of	(Note5)	T <sub>vj</sub> =150 °C	-	1.90	-	
	Collector-emitter saturation voltage	Ic=600 A,	T <sub>vj</sub> =25 °C	-	1.50	1.75	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.70	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.75	-	
Cies	Input capacitance			-	-	145.5	nF
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited	-	-	4.1		
Cres	Reverse transfer capacitance	7	-	-	1.8		
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V	-	4.5	-	μC	
t <sub>d(on)</sub>	Turn-on delay time	$V_{CC}$ =600 V, I <sub>C</sub> =600 A, $V_{GE}$ =±15 V, $R_{G}$ =1.0 Ω, Inductive load		-	-	600	ns
tr	Rise time			-	-	200	
t <sub>d(off)</sub>	Turn-off delay time			-	-	800	
t <sub>f</sub>	Fall time			-	-	400	
Nata1)		I <sub>E</sub> =600 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	1.75	2.25	V
V <sub>EC</sub> (Note1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.90	-	
(Terminal)	Emitter collector voltage	(Note5)	T <sub>vj</sub> =150 °C	-	1.95	-	
Nata1)	- Emitter-collector voltage	I <sub>E</sub> =600 A,	T <sub>vj</sub> =25 °C	-	1.60	1.95	
V <sub>EC</sub> (Note1) (Chip)		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.60	-	
(Criip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.60	-	
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	400	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	$R_G$ =1.0 Ω, Inductive load		-	46.8	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =600 A,	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =600 A,		50.6	-	m l
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=1.0 \Omega, T_{vj}=150 \text{ °C},$		-	61.9	-	mJ
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse	Inductive load		-	48.5	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =2	5 °C (Note4)	-	0.71	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	0.67	-	Ω

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HIGH POWER SWITCHING USE

**INSULATED TYPE** 

#### ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

#### NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	item	Conditions	Min.	Тур.	Max.	Offic
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions			Unit		
Symbol	item			Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)		-	-	48	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	76	N/KVV
D	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7)	i	11.5	-	K/kW
$R_{th(c-s)}$		per 1 module,	PC-TIM applied (Note4, 8)	ı	3.1	i	r/KVV

#### **MECHANICAL CHARACTERISTICS**

Cumala al	lá a sa	000		1.1-20			
Symbol	Item	Con	Min.	Тур.	Max.	Unit	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
		Caldennia tura (DV)	Terminal to terminal	17	-	-	mm
يا.	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	- mm
			Terminal to base plate	16.8	-	-	
		Solder pin type (DX)	Terminal to terminal	10	-	-	mm
يا.			Terminal to base plate	16.2	-	-	
d <sub>a</sub>	Clearance	5 (1) (2)(2)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP)  Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note9)		±0	-	+200	μm
m	mass	-		-	300	-	g

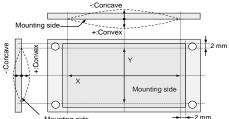
- \*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
  - 2. Junction temperature  $(T_{vj})$  should not increase beyond  $T_{vjmax}$  rating.
  - 3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  dose not exceed  $T_{vjmax}$  rating.
  - 4. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>S</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
  - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. 
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}$ =25 [°C]+273.15=298.15 [K]

 $R_{50}\!\!:$  resistance at absolute temperature  $T_{50}$  [K];  $T_{50}\!\!=\!\!50$  [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K)/D<sub>(C-S)</sub>=50  $\mu$ m.
- 8. Typical value is measured by using PC-TIM of  $\lambda$ =3.4 W/(m·K)/D<sub>(C·S)</sub>=50  $\mu$ m.
- 9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



#### <IGBT Modules>

## CM600DX-24T/CM600DXP-24T

#### HIGH POWER SWITCHING USE

#### INSULATED TYPE

Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t=1.6

	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N-III	

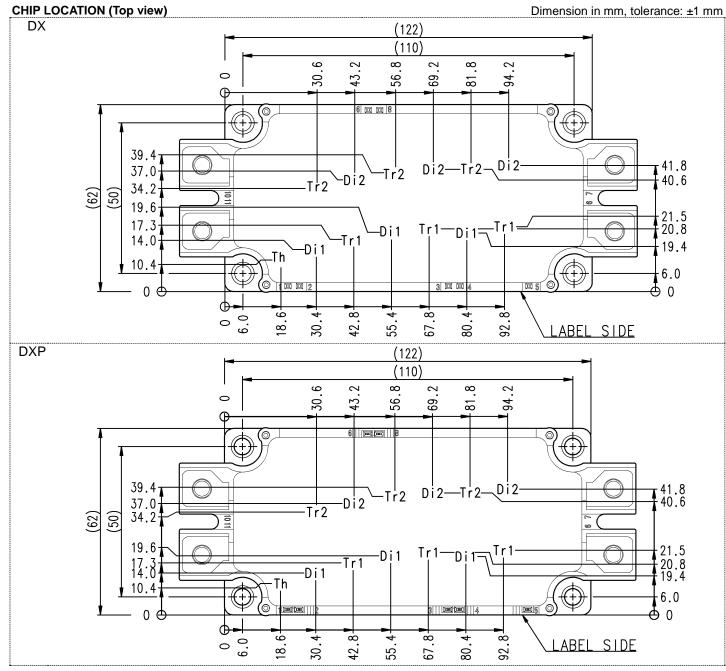
#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Item	Conditions		Unit		
Symbol	item	Conditions	Min.	Тур.	Max.	Onit
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	ı	600	850	٧
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	1.0	-	6.8	Ω

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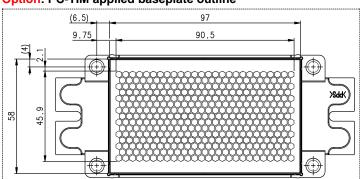
HIGH POWER SWITCHING USE

**INSULATED TYPE** 



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

#### **Option: PC-TIM applied baseplate outline**

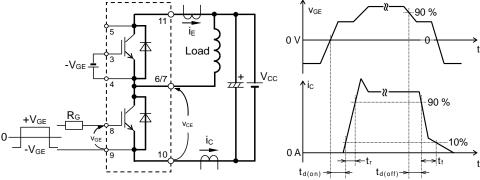


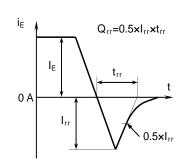
Ver.1.2

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

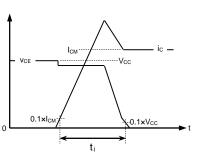
#### **TEST CIRCUIT AND WAVEFORMS**

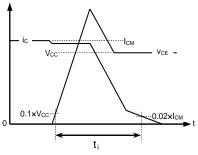


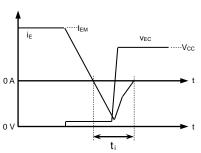


Switching characteristics test circuit and waveforms









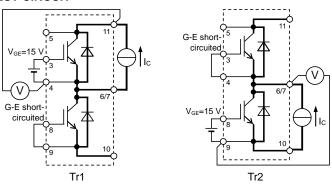
IGBT Turn-on switching energy

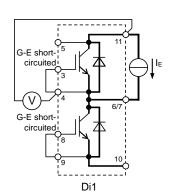
IGBT Turn-off switching energy

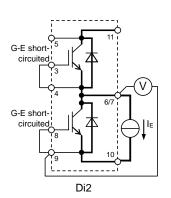
FWD Reverse recovery energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

#### **TEST CIRCUIT**







V<sub>CEsat</sub> characteristics test circuit

V<sub>EC</sub> characteristics test circuit

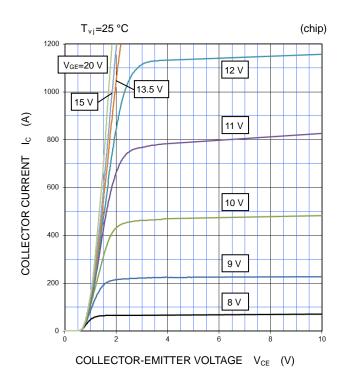
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

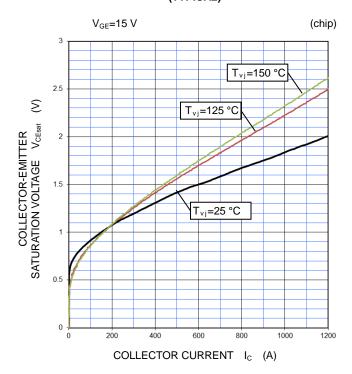
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

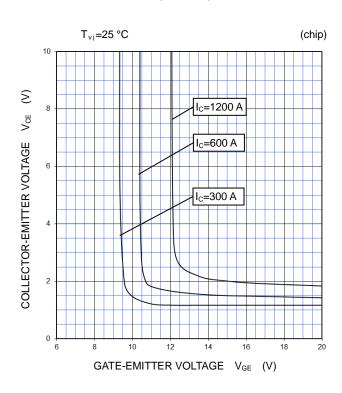
## OUTPUT CHARACTERISTICS (TYPICAL)



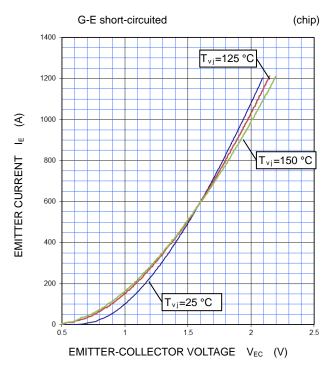
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



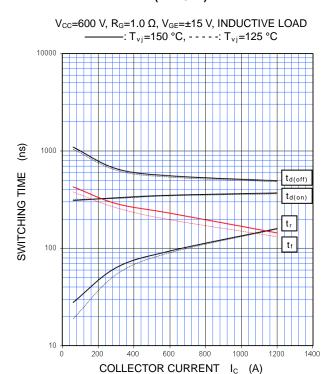
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

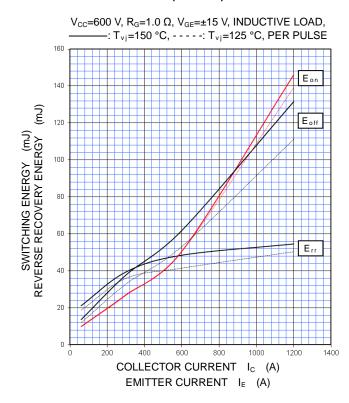
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

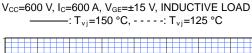
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

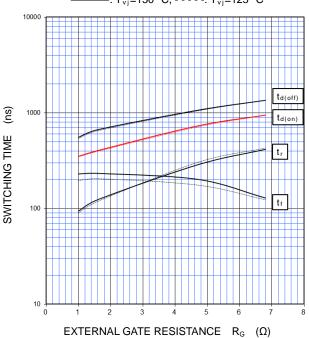


#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



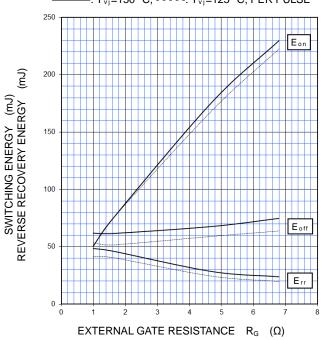
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =600 V,  $I_C/I_E$ =600 A,  $V_{GE}$ =±15 V, INDUCTIVE LOAD, -: T<sub>vj</sub>=150 °C, - - - - -: T<sub>vj</sub>=125 °C, PER PULSE



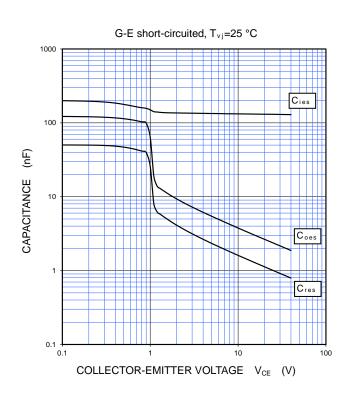
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

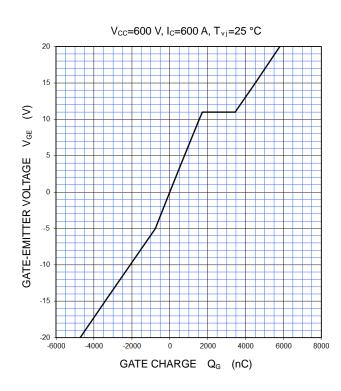
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

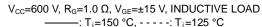
## CAPACITANCE CHARACTERISTICS (TYPICAL)

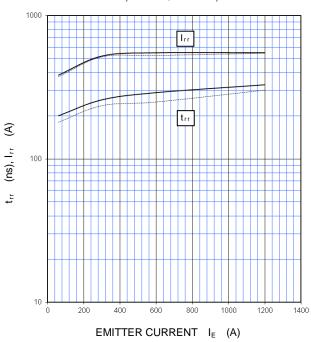


## GATE CHARGE CHARACTERISTICS (TYPICAL)

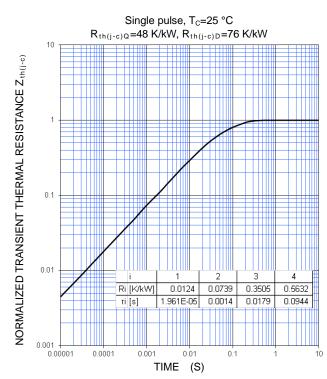


# FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)





## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



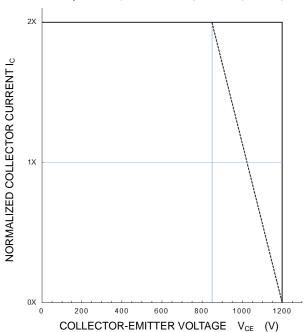
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

#### **PERFORMANCE CURVES**

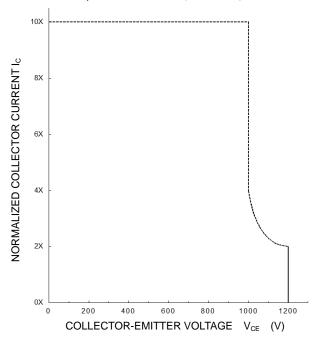
#### **INVERTER PART**

#### TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



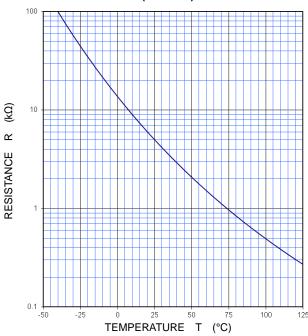
## SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 800 \text{ V}$ ,  $R_G = 1.0 \sim 6.8 \Omega$ ,  $V_{GE} = \pm 15 \text{ V}$ ,  $T_{vj} = 25 \sim 150 \text{ °C}$ ,  $t_W \le 8 \mu \text{s}$ , Non-Repetitive



#### NTC thermistor part

## TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

### Keep safety first in your circuit designs!

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